

predetermined signal used for generating a common signal and multiple pixel signals to be supplied to the multiple pixels, wherein the predetermined signal is embedded into a predetermined period between a group of pixel signals and another group of pixel signals in the display signal, and the common signal is to be commonly supplied to the multiple pixels. Claims 2-4, 8, and 12 depend from claim 1.

Independent claim 16 is directed to an image processing device that generates a display signal, which is to be input into a liquid crystal device having multiple pixels, the image processing device comprising: a video signal conversion circuit that converts an input video signal and generates multiple pixel signals, which are to be given to the multiple pixels of the liquid crystal device; and a display signal generation circuit that combines the multiple pixel signals with a predetermined signal, which is used for generating a common signal to be commonly supplied to the multiple pixels, and thereby generates one display signal.

Independent claims 19 and 20 are directed to a method of inputting a predetermined signal into a liquid crystal device, and an image processing method, respectively.

Such liquid crystal devices, image processing devices, method of inputting a predetermined signal into a liquid crystal device, and image processing method, are nowhere disclosed in Hoshi.

Hoshi is cited as disclosing a liquid crystal device having multiple pixels, in Fig. 4. The Office Action argues that Hoshi discloses that the liquid crystal device comprises an input terminal that receives a display signal including a predetermined signal used for generating a common signal and multiple pixel signals to be supplied to the multiple pixels, at Figs. 4 and 7 and at col. 2, line 63 to col. 3, line 20, and wherein the predetermined signal is embedded into a predetermined period between a group of pixel signals and another group of pixel signals in the display signal, and the common signal is to be commonly supplied to

the multiple pixels, at Figs. 4 and 7 and at col. 2, line 63 to col. 3, line 20, col. 3, line 41 to col. 4, line 60. Applicant disagrees.

At col. 3, lines 27-28, Hoshi specifically discloses that "26 denotes an input terminal for video signal polarity change-over signals." Hoshi goes on to describe that "initially a positive polarity video signal a is applied to the terminal 26. When a signal b, which inverts at the period of 1 field or an integral multiple of 1 field, is applied to the terminal 26, a video signal c ... is applied to the signal line driver 22." Col. 3, lines 31-38. Further, Hoshi discloses at col. 3, lines 53-60, that "[f]urther the inversion signal b is applied to the serial input terminal 20 of the common electrode line driver 19, a pulse f having a period $3T$ is applied to the clock input terminal 21, and the electric potentials of each of the common electrode lines $z_j, z_{j+1} \dots$ synchronize with the pulse f per each 1 field or each integral multiple of 1 field and repeat the inversion between the high voltage V_H and the low voltage V_L in turn."

From these disclosures, it is clear that Hoshi uses a video signal polarity change-over signal to generate a common signal. However, that common signal is not embedded into a video signal that is a display signal. Instead, the video signal polarity change-over signal of Hoshi is independent of the video signal.

In contrast, the instant claims require that a predetermined signal used for generating a common signal is embedded into a display signal. See independent claims 1 and 19. Alternatively, the instant claims require the method steps that "a display signal generation circuit that combines the multiple pixel signals with a predetermined signal, which is used for generating a common signal to be commonly supplied to the multiple pixels" (claim 16) or "combining the multiple pixel signals with a predetermined signal, which is used for generating a common signal to be commonly supplied to the multiple pixels" (claim 20).

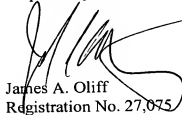
Hoshi nowhere discloses these claimed features where a predetermined signal used for generating a common signal is embedded or combined into a display signal, as claimed.

Accordingly, because Hoshi does not disclose all of the limitations of the claimed invention, the claims are not anticipated by Hoshi. Reconsideration and withdrawal of the rejection are respectfully requested.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of the claims are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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